

Datasheet

10 Gbps Ethernet Single-Mode XFP Transceivers

XFP-10GED-LR



Highlights

- XFP transceiver
- Data Rates: 10.3125 Gbps
- Protocol:
 - 10 Gigabit Ethernet
- Single-mode dual-fiber (Tx/Rx)
- 1310 nm
- 0 to 10 km range
- Duplex LC connector
- Digital Diagnostics (SFF-8472)
- XFI Loopback
- Hot-swap
- Industrial temperature models available

Overview

MRV Communications' XFP transceivers provide the high speeds and compact dimensions that today's demanding networks require while delivering the deployment flexibility and inventory control that network administrators demand. Designed to Multi-Source Agreement (MSA) standards for broadest compatibility, they perfectly match MRV's wide range of optical transport solutions.

Visit the MRV website at www.mrv.com or contact your nearest authorized MRV Communications dealer for more information.

Specifications Overview

Data Rate	10.3125 Gbps
Tx Wavelength	1310 nm
Tx Power (Minimum OMA)	-5.2 dBm
Dispersion Penalty	3.2 dB
Tx Disable	Yes
Rx Wavelength Range	1260 - 1600 nm
Rx Sensitivity (OMA)	-12.6 dBm
Rx Saturation	0.5 dBm
Operating Temperature Range	0 to 70 °C
Operating Temperature Range (TH Models)	-40 to 80 °C
Power Consumption	2.65 Watts

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Optical Specifications

Parameter	Symbol	Minimum	Maximum	Unit	Notes
Operating Data Rate	DR	-	10.3125	Gbps	-
Transmitter					
Center Wavelength	λ_C	1260	1355	nm	-
Average Output Power	P_{OUT}	-8.2	0.5	dBm	1
Optical Modulation Amplitude	OMA	-5.2	-	dBm	1
Spectral Width	$\Delta\lambda$	-	1	nm	-
Side Mode Suppression Ration	SMSR	30	-	dB	-
Extinction Ratio	ER	3.5	-	dB	2
Dispersion Penalty	DP	-	3.2	dB	2
Optical Eye Mask	Compliant with IEEE 802.3ae				
Receiver					
Center Wavelength	λ_C	1260	1600	nm	-
Receiver Sensitivity	P_{IN}	-	-14.4	dBm	3
Receiver Sensitivity in OMA	P_{IN}	-	-12.6	dBm	3
Receiver Overload	P_{IN}	0.5	-	dBm	3
LOS Assert	LOS_A	-25	-	dBm	-
LOS De-Assert	LOS_D	-	-15	dBm	-
LOS Hysteresis	-	1	4	dB	-
Receiver Reflectance	-	-	-12	dB	-

- Notes:**
1. The optical power is launched into SMF.
 2. Measured with a PRBS 2³¹-1 test pattern @ 10.3125 Gbps.
 3. Measured with a PRBS 2³¹-1 test pattern @ 10.3125 Gbps, BER ≤ 10⁻¹².

Monitoring Specifications

Data Address	Parameter	Range	Accuracy
96-97	Temperature	-10 to 80 °C (Standard) -40 to 90 °C (Industrial)	± 3 °C
100-101	Bias Current	0 to 100 mA	± 10 %
102-103	TX Power	-9 to 1 dBm	± 2 dB
104-105	RX Power	-16 to 0 dBm	± 2 dB
106-107	V _{CC5} Voltage	+4.5 V to +5.5 V	± 3 %
108-109	V _{CC3} Voltage	+3.0 V to +3.7 V	± 3 %

Absolute Maximum Rating

Parameter	Symbol	Min.	Max.	Unit	Notes
Supply Voltage (3.3V)	V _{CC3}	-0.5	4.0	V	-
Supply Voltage (5.0V)	V _{CC5}	-0.5	6.0	V	-
Operating Relative Humidity	RH	-	85	%	-
Storage Temperature	T _s	-40	85	°C	-

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Recommended Operation Conditions

Parameter	Symbol	Min	Max	Unit	Notes
Operating Case Temperature	T_C	0	70	°C	-
Operating Case Temperature (TH Models)	T_C	-40	80	°C	-
Power Supply Voltage (3.3V)	V_{CC3}	3.13	3.47	V	-
Power Supply Voltage (5V)	V_{CC5}	4.75	5.25	V	-
Power Supply Current (3.3V)	I_{CC3}	-	500	mA	-
Power Supply Current (5V)	I_{CC5}	-	200	mA	-
Power Dissipation	P_D	-	2.5	W	-
Data Rate	DR	-	10.3125	Gbps	-
Transmission Distance	-	2	10,000	m	1

Notes: 1. Tested over single-mode fiber

Electrical Specifications

Parameter	Symbol	Minimum	Maximum	Unit	Notes
High-Speed Signal (CML) Interface Specification					
Input Data Rate	-	-	10.3125	Gbps	-
Differential Data Input Amplitude	-	120	1200	mVpp	1
Input Differential Impedance	-	80	120	Ω	-
Output Data Rate	-	-	10.3125	Gbps	-
Differential Data Output Amplitude	-	500	800	mVpp	1
Output Differential Impedance	-	80	120	Ω	-
Low-Speed Signal (LVTTTL) Interface Specification					
Input High Voltage	-	2.0	Vdd1=3.3	V	-
Input Low Voltage	-	GND	0.8	V	-
Output High Voltage	-	2.4	Vdd1=3.3	V	-
Output Low Voltage	-	GND	0.4	V	-
2-Wire Serial Interface(LVTTTL) Specification					
Clock Frequency	f_{SCL}	-	400	kHz	-
Reference Clock (PECL) Interface Specification					
No Reference Clock Needed					

Notes: 1. Internally AC coupled.

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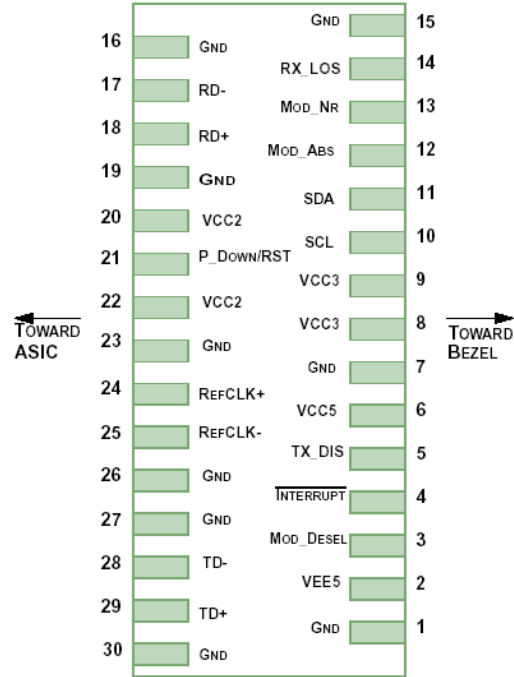
Host Board Connector Legend

Pin	Logic	Symbol	Name/Description	Note
1	-	GND	Module Ground	1
2	-	V _{EE5}	Optional -5.2 V Power Supply (Not Implemented)	-
3	LVTTTL-I	Mod_DeSel	Module De-Select; When Held Low Allows Module to Respond to 2-wire Serial Interface	-
4	LVTTTL-O	$\overline{\text{Interrupt}}$	$\overline{\text{Interrupt}}$; Indicates Presence of an Important Condition Which Can Be Read Over the 2-Wire Serial Interface	2
5	LVTTTL-I	TX_DIS	Transmitter Disable; Turns Off Transmitter Laser Output	-
6	-	V _{CC5}	+5 V Power Supply	-
7	-	GND	Module Ground	1
8	-	V _{CC3}	+3.3 V Power Supply	-
9	-	V _{CC3}	+3.3 V Power Supply	-
10	LVTTTL-I/O	SCL	2-Wire Serial Interface Clock	2
11	LVTTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
12	LVTTTL-O	Mod_Abs	Indicates Module is Not Present. Grounded in the Module	2
13	LVTTTL-O	Mod_NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTTL-O	RX_LOS	Receiver Loss of Signal Indicator	2
15	-	GND	Module Ground	1
16	-	GND	Module Ground	1
17	CML-O	RD-	Receiver Inverted Data Output	-
18	CML-O	RD+	Receiver Non-Inverted Data Output	-
19	-	GND	Module Ground	1
20	-	V _{CC2}	+1.8 V Power Supply (Not Implemented)	3
21	LVTTTL-I	P_Down/RST	Power Down; When High, Requires the Module to Limit Power Consumption to 1.5 W or Below. 2-Wire Serial Interface Must Be Functional in the Low Power Mode.	-
			Reset; The Falling Edge Initiates a Complete Reset of the Module Including the 2-Wire Serial Interface, Equivalent to a Power Cycle.	
22	-	V _{CC2}	+1.8 V Power Supply (Not Implemented)	3
23	-	GND	Module Ground	1
24	PECL-I	RefCLK+	Not Used, Internally Terminated to 50 Ohm (100 Ohm Diff).	4
25	PECL-I	RefCLK-	Not Used, Internally Terminated to 50 Ohm (100 Ohm Diff).	4
26	-	GND	Module Ground	1
27	-	GND	Module Ground	1
28	CML-I	TD-	Transmitter Inverted Data Input	
29	CML-I	TD+	Transmitter Non-Inverted Data Input	
30	-	GND	Module Ground	1

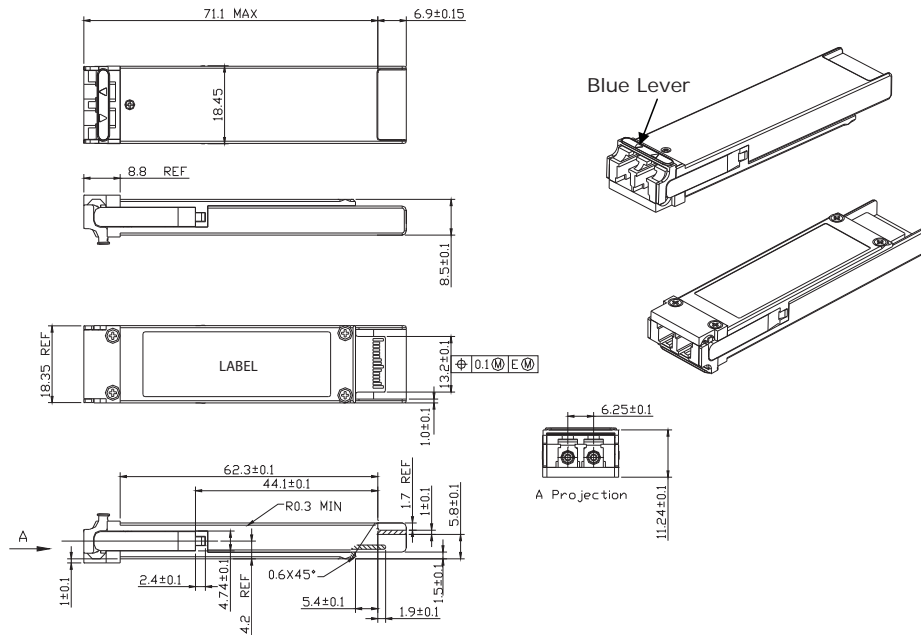
- Note:
1. Module ground pins GND are isolated from the module case and chassis ground within the module.
 2. Shall be pulled up with 4.7 K-10 Kohms to a voltage between 3.15 V and 3.45 V on the host board.
 3. The pins are open within module.
 4. Reference Clock is not required.

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Host Board Connector Pinout



Mechanical Drawing





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Ordering Information

Model	Description	Data Rate (Gbps)	Wavelength (nm)	Dispersion Penalty (dB)	Bail Latch Color	Distance Range (km)
XFP-10GED-LR	10 GbE single-mode XFP transceiver with Digital Diagnostics.	10.3125	1310	3.2	Blue	0 - 10
XFP-10GED-LRTH	10 GbE single-mode XFP transceiver with Digital Diagnostics, <i>Temperature Hardened</i>	10.3125	1310	3.2	Blue	0 - 10

Regulatory and Industry Compliances

Class 1 Laser Product, complies with EN 60825-1 and 21 CFR 1040.10 except for deviations pursuant to Laser Notice No. 50. dated June 24, 2007
MSA INF-8077i; Digital Diagnostic SFF-8472

Certified by one or more of the following agencies: TÜV, UL, CSA

RoHS Directive; China RoHS; California RoHS Law, REACH Directive SVHC; WEEE Directive

The Quality Management System is certified to ISO 9001 by QMI-SAI Global

The Environmental Management System is in compliance with ISO 14001

Warnings

Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures.

Laser Safety: Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.

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